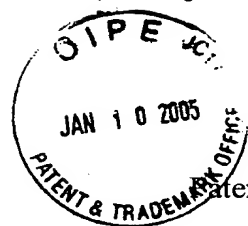


09/673, 208.

PATENT

CofC



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,798,058 B1

Issue Date: September 28, 2004

Inventor(s): Nobuaki HASHIMOTO

Title: SEMICONDUCTOR DEVICE, MOUNTING SUBSTRATE AND  
METHOD OF MANUFACTURING MOUNTING SUBSTRATE, CIRCUIT  
BOARD, AND ELECTRONIC INSTRUMENT

Docket No.: 107281

**REQUEST FOR CERTIFICATE OF CORRECTION UNDER RULE 322**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

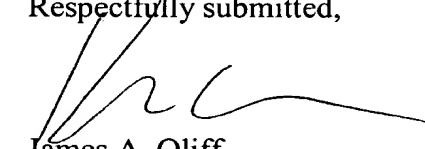
**Certificate**  
**JAN 18 2005**  
**of Correction**

Sir:

It is respectfully requested that a Certificate of Correction be issued in order to correct the error(s) specified in the attached copy of Certificate of Correction Form PTO-1050.

It is believed that the errors are on the part of the Patent and Trademark Office, and therefore no fee is due in relation to this matter in accordance with the provisions of 37 C.F.R. §1.322. However, should any fee be due, please charge the same against Deposit Account No. 15-0461 in order to ensure prompt issuance of a Certificate of Correction.

Respectfully submitted,

  
James A. Oliff  
Registration No. 27,075

Kevin M. McKinley  
Registration No. 43,794

JAO:KMM/scg

**19 JAN 2005**

Date: January 10, 2005

OLIFF & BERRIDGE, PLC  
P.O. Box 19928  
Alexandria, Virginia 22320  
Telephone: (703) 836-6400

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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,798,058 B1  
DATED : September 28, 2004  
INVENTOR(S) : Nobuaki Hashimoto

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page

Please correct the title to read --(54) SEMICONDUCTOR DEVICE, MOUNTING  
SUBSTRATE AND METHOD OF MANUFACTURING MOUNTING SUBSTRATE,  
CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT--

MAILING ADDRESS OF SENDER: OLIFF & BERRIDGE, PLC (107281 KMM/SCG)  
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PATENT NO. 6,798,058 B1  
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